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EXAMINER

ZERVIGON, RUDY

ART UNIT

PAPER NUMBER

1763

MAIL DATE

DELIVERY MODE

06/04/2007

PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary

Application No.

09/925,579

Applicant(s)

NAKANO ET AL.

Examiner

Rudy Zervigon

Art Unit

1763

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 23 March 2007.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-8 and 63-68 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-8 and 63-68 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)
Paper No(s)/Mail Date <u>3/14/2007</u> . | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Claim Rejections - 35 USC § 103

1. The text of those sections of Title 35, U.S. Code not included in this action can be found in a prior Office action.
2. Claims 1-6, 8, 9, and 64-68 are rejected under 35 U.S.C. 103(a) as being unpatentable over Murata et al (USPat. 5,423,915) in view of Patrick (USPat. 5,474,648). Murata et al (USPat. 5,423,915) teaches a plasma processing apparatus (Figure 1; column 5; line 44 - column 6; line 11) comprising: a plasma processing chamber (1; Figure 1; column 5; line 44 - column 6; line 11) having a plasma excitation electrode (2; Figure 1; column 5; line 44 - column 6; line 11) for exciting a plasma; a radio frequency generator (4; Figure 1; column 5; line 44 - column 6; line 11) for supplying a radio frequency voltage to the electrode (2; Figure 1; column 5; line 44 - column 6; line 11); a radio frequency feeder (105; Figure 1; column 5; line 44 - column 6; line 11) connected to the electrode (2; Figure 1; column 5; line 44 - column 6; line 11); and a matching circuit (104; Figure 1; column 5; line 44 - column 6; line 11) having an input terminal (104/4 interface; Figure 1; column 5; line 44 - column 6; line 11) and an output (106, 109; Figure 1; column 5; line 44 - column 6; line 11) end, wherein the input terminal (104/4 interface; Figure 1; column 5; line 44 - column 6; line 11) is connected to the radio frequency generator (4; Figure 1; column 5; line 44 - column 6; line 11) and the output (106, 109; Figure 1; column 5; line 44 - column 6; line 11) end is connected to an end of the radio frequency feeder (105; Figure 1; column 5; line 44 - column 6; line 11) so as to achieve impedance matching between the plasma processing chamber (1; Figure 1; column 5; line 44 - column 6; line 11) and the radio frequency generator (4; Figure 1; column 5; line 44 - column 6; line 11) – claim 1

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Murata further teaches applying a frequency of 13.56MHz (column 5; lines 48-55) to both the plasma processing chamber (1; Figure 1; column 5; line 44 - column 6; line 11) and the plasma excitation electrode (2; Figure 1; column 5; line 44 - column 6; line 11).

Murata further teaches:

- i. A plasma processing apparatus (Figure 1; column 5; line 44 - column 6; line 11) comprising: a plasma processing chamber (1; Figure 1; column 5; line 44 - column 6; line 11) having a first series resonant frequency f_0 and a plasma excitation electrode (2; Figure 1; column 5; line 44 - column 6; line 11) for exciting a plasma; a radio frequency generator (4; Figure 1; column 5; line 44 - column 6; line 11) for supplying a radio frequency voltage to the electrode (2; Figure 1; column 5; line 44 - column 6; line 11); a radio frequency feeder (105; Figure 1; column 5; line 44 - column 6; line 11) connected to the electrode (2; Figure 1; column 5; line 44 - column 6; line 11); and a matching circuit (104; Figure 1; column 5; line 44 - column 6; line 11) having an input terminal (104/4 interface; Figure 1; column 5; line 44 - column 6; line 11) and an output (106, 109; Figure 1; column 5; line 44 - column 6; line 11) end, wherein the input terminal (104/4 interface; Figure 1; column 5; line 44 - column 6; line 11) is connected to the radio frequency generator (4; Figure 1; column 5; line 44 - column 6; line 11) and the output (106, 109; Figure 1; column 5; line 44 - column 6; line 11) end is connected to an end of the radio frequency feeder (105; Figure 1; column 5; line 44 - column 6; line 11) so as to achieve impedance matching between the plasma processing chamber (1; Figure 1; column 5; line 44 - column 6; line 11) and the radio frequency generator (4; Figure 1; column 5; line 44 - column 6; line 11) – claim 66

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Murata further teaches that at least one of the shape of a feed plate, the overlap area (column 8; lines 45-59) of the plasma excitation electrode and a chamber wall, insulation material between the plasma excitation electrode and the chamber wall, or the capacitance (column 8; lines 45-59) between a susceptor electrode and the chamber wall are considered result-effective variables for film thickness distribution and film forming speed as taught by Murata (column 8; lines 45-59).

Murata does not teach a frequency which is three times a first series resonant frequency f_0 of the plasma processing chamber (1; Figure 1; column 5; line 44 - column 6; line 11) is larger than a power frequency f_e of the radio frequency waves at the end of the radio frequency feeder, and wherein the first series resonant frequency f_0 based on the measured impedance of the path from the radio frequency feeder to the ground via a shaft and a variable oscillation frequency when the plasma processing chamber is disconnected from the matching circuit and the first series resonant frequency f_0 corresponds to a minimum impedance of the plasma processing chamber, when the plasma chamber is disconnected from the plasma apparatus during a non-discharge period – claim 1. Applicant's claim limitation of "the first series resonant frequency f_0 based on the measured impedance of the path from the radio frequency feeder to the ground via a shaft and a variable oscillation frequency when the plasma processing chamber is disconnected from the matching circuit and the first series resonant frequency f_0 corresponds to a minimum impedance of the plasma processing chamber, when the plasma chamber is disconnected from the plasma apparatus during a non-discharge period" and "and a set of electrical radio frequency factors of the plasma processing chamber configured such that at an end of the radio frequency feeder" appear to be a claim recitation of intended use in the pending apparatus claims. Further, it has been held that claim language that simply specifies an intended use or field of use for the

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invention generally will not limit the scope of a claim (Walter , 618 F.2d at 769, 205 USPQ at 409; MPEP 2106). Additionally, in apparatus claims, intended use must result in a structural difference between the claimed invention and the prior art in order to patentably distinguish the claimed invention from the prior art. If the prior art structure is capable of performing the intended use, then it meets the claim (In re Casey, 152 USPQ 235 (CCPA 1967); In re Otto , 136 USPQ 458, 459 (CCPA 1963); MPEP 2111.02).

Murata further does not teach:

- i. A plasma processing apparatus (Figure 1; column 5; line 44 - column 6; line 11) according to claim 1, wherein a frequency of 1.3 times the first series resonant frequency f_0 is larger than a power frequency f_e - claim 2
- ii. A plasma processing apparatus (Figure 1; column 5; line 44 - column 6; line 11) according to claim 2, wherein the first series resonant frequency f_0 is larger than three times the power frequency f_e . - claim 3
- iii. A plasma processing apparatus (Figure 1; column 5; line 44 - column 6; line 11) according to claim 3, wherein a series resonant frequency f_0 , which is defined by a capacitance between the plasma excitation electrode (2; Figure 1; column 5; line 44 - column 6; line 11) and a counter electrode (3; Figure 1; column 5; line 44 - column 6; line 11) for generating the plasma in cooperation with the plasma excitation electrode (2; Figure 1; column 5; line 44 - column 6; line 11) is larger than three times the power frequency f_e . - claim 4
- iv. A plasma processing apparatus (Figure 1; column 5; line 44 - column 6; line 11) according to claim 4, wherein the plasma excitation electrode (2; Figure 1; column 5; line

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44 - column 6; line 11) and the counter electrode (3; Figure 1; column 5; line 44 - column 6; line 11) are of a parallel plate type, and the series resonant frequency f_0 , and the power frequency f_e satisfy the relationship:

$$f_0 > \sqrt{\frac{d}{\delta}} f_e$$

wherein d represents the distance between the plasma excitation electrode (2; Figure 1; column 5; line 44 - column 6; line 11) and the counter electrode (3; Figure 1; column 5; line 44 - column 6; line 11), and δ represents the sum of the distance between the plasma excitation electrode (2; Figure 1; column 5; line 44 - column 6; line 11) and the generated plasma and the distance between the counter electrode (3; Figure 1; column 5; line 44 - column 6; line 11) and the generated plasma – claim 5

Murata further does not teach:

- v. A plasma processing apparatus (Figure 1; column 5; line 44 - column 6; line 11) according to claim 1, further comprising a resonant frequency measuring terminal for measuring the resonant frequency of the plasma processing chamber (1; Figure 1; column 5; line 44 - column 6; line 11), in the vicinity of the end of the radio frequency feeder (105; Figure 1; column 5; line 44 - column 6; line 11) – claim 6
- vi. A plasma processing apparatus (Figure 1; column 5; line 44 - column 6; line 11) according to claim 6, further comprising a resonant frequency measuring unit which is detachably connected to the resonant frequency measuring terminal – claim 8
- vii. A plasma processing apparatus (Figure 1; column 5; line 44 - column 6; line 11) according to claim 8, wherein the resonant frequency characteristics in the plasma

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excitation mode and the resonant frequency characteristics in the measuring mode are set to be equal to each other – claim 9

- viii. The plasma processing apparatus (Figure 1; column 5; line 44 - column 6; line 11) according to claim 1, wherein the plasma excitation electrode (2; Figure 1; column 5; line 44 - column 6; line 11) comprises an overlapping area (projection of 2 onto 1; Figure 1) with respect to the chamber wall, the overlapping area (projection of 2 onto 1; Figure 1) adapted to set the first series resonant frequency f_0 such that three times the first series resonant frequency f_0 is larger than a power frequency f_e supplied from the radio frequency generator (4; Figure 1; column 5; line 44 - column 6; line 11), as claimed by claim 64
- ix. The plasma processing apparatus (Figure 1; column 5; line 44 - column 6; line 11) according to claim 1, wherein the radio frequency feeder (105; Figure 1; column 5; line 44 - column 6; line 11) has a length adapted to set the first series resonant frequency f_0 such that three times the first series resonant frequency f_0 is larger than a power frequency f_e supplied from the radio frequency generator (4; Figure 1; column 5; line 44 - column 6; line 11), as claimed by claim 65
- x. wherein the first series resonant frequency f_0 corresponds to a minimum impedance of the plasma processing chamber (1; Figure 1; column 5; line 44 - column 6; line 11), the minimum impedance evaluated with the plasma chamber disconnected from the plasma apparatus during a non-discharge period, and wherein at least one of the shape of a feed plate, the overlap area of the plasma excitation electrode and a chamber wall, insulation material between the plasma excitation electrode and the chamber wall, or the

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capacitance between a susceptor electrode and the chamber wall is adjusted such that three times the first series resonant frequency f_0 is larger than a power frequency f_e supplied from the radio frequency generator (4; Figure 1; column 5; line 44 - column 6; line 11) – claim 66

- xi. The plasma processing apparatus (Figure 1; column 5; line 44 - column 6; line 11) according to claim 66, wherein at least one of the shape of the feed plate, the overlap area of the plasma excitation electrode and a chamber wall, insulation material between the plasma excitation electrode and the chamber wall, or the capacitance between a susceptor electrode and the chamber wall is adjusted such that 1.3 times the first series resonant frequency f_0 is larger than a power frequency f_e , as claimed by claim 67
- xii. The plasma processing apparatus (Figure 1; column 5; line 44 - column 6; line 11) according to claim 67, wherein at least one of the shape of the feed plate, the overlap area of the plasma excitation electrode and a chamber wall, insulation material between the plasma excitation electrode and the chamber wall, or the capacitance between a susceptor electrode and the chamber wall is adjusted such that the first series resonant frequency f_0 is larger than a power frequency f_e , as claimed by claim 68

Patrick (USPat. 5,474,648) teaches a plasma reactor (104, Figure 2a; column 6; line 54 – column 7; line 25) including a variable RF parameter sensor (202; Figure 2a) which measures power, voltage, current, phase angle, harmonic content (abstract), and impedance parameters at the plasma chamber electrode (112; Figure 2a, claim 5). That Patrick et al measures a frequency, resonant or otherwise, at the plasma chamber electrode is inherent because the applied frequency is that of the dynamic voltage and current that are measured and dynamically controlled (claim

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6). The Examiner believes Patrick's apparatus is inherent in setting a frequency f_0 corresponding desired, or optimized values, including "corresponding" a minimum impedance (as measured by Patrick) of the plasma processing chamber. That Patrick can measure the minimum impedance with the plasma chamber disconnected from the plasma apparatus during a non-discharge period, is a claim requirement of intended use. See above.

Patrick further teaches that his plasma processing apparatus (Figure 2a; column 6; line 54 – column 7; line 25) produces frequencies which is defined by a capacitance between the plasma excitation electrode (112; Figure 2a) and a counter electrode (114; Figure 2a) for generating the plasma in cooperation with the plasma excitation electrode (112; Figure 2a). Further when the structure recited in the references is substantially identical to that of the claims, claimed properties or functions are presumed to be inherent. Where the claimed and prior art products are identical or substantially identical in structure or composition, or are produced by identical or substantially identical processes, a prima facie case of either anticipation or obviousness has been established. In re Best, 562 F.2d 1252, 1255, 195 USPQ 430, 433 (CCPA1977) – MPEP 2114.

It would have been obvious to one of ordinary skill in the art at the time the invention was made for Murata to use Patrick et al's system for plasma dynamic control including optimizing the relative frequencies between Murata's plasma excitation electrode and Murata's radio frequency generator depending on the geometry of the plasma chamber and dynamic processing conditions. Motivation for Murata to use Patrick et al's system for plasma dynamic control including optimizing the relative frequencies between Murata's plasma excitation electrode and Murata's

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radio frequency generator depending on the geometry of the plasma chamber and dynamic processing conditions is for enabling the repeatability and uniformity of plasma processing as taught by Patrick et al (column 3; lines 55-65) and Murata (column 8; lines 45-59).

It would be obvious to those of ordinary skill in the art to optimize the operation of the claimed invention (In re Boesch, 617 F.2d 272, 205 USPQ 215 (CCPA 1980); In re Hoeschele, 406 F.2d 1403, 160 USPQ 809 (CCPA 1969); Merck & Co. Inc. v. Biocraft Laboratories Inc., 874 F.2d 804, 10 USPQ2d 1843 (Fed. Cir.), cert. denied, 493 U.S. 975 (1989); In re Kulling, 897 F.2d 1147, 14 USPQ2d 1056 (Fed. Cir. 1990), MPEP 2144.05).

3. Claim 7 is rejected under 35 U.S.C. 103(a) as being unpatentable over Murata et al (USPat. 5,423,915) and Patrick (USPat. 5,474,648) in view of Stramke (USPat. 4,645,981). Murata and Patrick are discussed above. Murata and Patrick do not teach a switch provided between Murata's radio frequency feeder (105; Figure 1; column 5; line 44 - column 6; line 11) and a resonant frequency measuring terminal, wherein the switch electrically disconnects the end of Murata's radio frequency feeder (105; Figure 1; column 5; line 44 - column 6; line 11) from a resonant frequency measuring terminal and connects the end of Murata's radio frequency feeder (105; Figure 1; column 5; line 44 - column 6; line 11) to Murata's output (106, 109; Figure 1; column 5; line 44 - column 6; line 11) end of Murata's matching circuit (104; Figure 1; column 5; line 44 - column 6; line 11) in a plasma excitation mode in which the plasma is excited, whereas the switch electrically connects the end of Murata's radio frequency feeder (105; Figure 1; column 5; line 44 - column 6; line 11) to the resonant frequency measuring terminal and disconnects the end of Murata's radio frequency feeder (105; Figure 1; column 5; line 44 - column 6; line 11) from the resonant frequency measuring terminal in a measuring mode in

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which the resonant frequency of the plasma processing chamber (1; Figure 1; column 5; line 44 - column 6; line 11) is measured.

Stramke teaches a capacitive plasma processing apparatus (Figure 1; column 3; line 57 – column 4, line 19) including a switch (“S1”; Figure 1; column 3; line 57 – column 4, line 19) for a current sensor (12; Figure 1; column 3; line 57 – column 4, line 19).

It would have been obvious to one of ordinary skill in the art at the time the invention was made for Murata and Patrick to add a switch to the RF parameter sensor as taught by Stramke.

Motivation for Murata and Patrick to add a switch to the RF parameter sensor as taught by Stramke is to allow for current sampling durations as taught by Stramke (column 4; lines 46-50).

4. Claims 10, 63 are rejected under 35 U.S.C. 103(a) as being unpatentable over Murata et al (USPat. 5,423,915) in view of Patrick (USPat. 5,474,648) and Hoke; William E. et al. (US 5077875 A). Murata and Patrick are discussed above, however, Murata teaches a plasma processing apparatus (Figure 1; column 5; line 44 - column 6; line 11) comprising: a plasma processing chamber (1; Figure 1; column 5; line 44 - column 6; line 11) having a plasma excitation electrode (2; Figure 1; column 5; line 44 - column 6; line 11) for exciting a plasma, a counter electrode (3; Figure 1; column 5; line 44 - column 6; line 11); a radio frequency generator (4; Figure 1; column 5; line 44 - column 6; line 11) for supplying a radio frequency voltage to the plasma excitation electrode (2; Figure 1; column 5; line 44 - column 6; line 11); a radio frequency feeder (105; Figure 1; column 5; line 44 - column 6; line 11) connected to the plasma excitation electrode (2; Figure 1; column 5; line 44 - column 6; line 11); and a matching circuit (104; Figure 1; column 5; line 44 - column 6; line 11) having an input terminal (104/4 interface; Figure 1; column 5; line 44 - column 6; line 11) and an output (106, 109; Figure 1;

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column 5; line 44 - column 6; line 11) end, wherein the input terminal (104/4 interface; Figure 1; column 5; line 44 - column 6; line 11) is connected to the radio frequency generator (4; Figure 1; column 5; line 44 - column 6; line 11) and the output (106, 109; Figure 1; column 5; line 44 - column 6; line 11) end is connected to an end of the radio frequency feeder (105; Figure 1; column 5; line 44 - column 6; line 11) so as to achieve impedance matching between the plasma processing chamber (1; Figure 1; column 5; line 44 - column 6; line 11) and the radio frequency generator (4; Figure 1; column 5; line 44 - column 6; line 11) – claim 63

Murata does not teach:

- i. a shower plate disposed between the plasma excitation electrode (2; Figure 1; column 5; line 44 - column 6; line 11) and the counter electrode (3; Figure 1; column 5; line 44 - column 6; line 11) – claim 63
- ii. wherein a frequency which is three times a first series resonant frequency f_0 of the plasma processing chamber (1; Figure 1; column 5; line 44 - column 6; line 11) which is measured at the end of the radio frequency feeder (105; Figure 1; column 5; line 44 - column 6; line 11) is larger than a power frequency f_e of the radio frequency waves, and wherein the first series resonant frequency f_0 is determined the first series resonant frequency f_0 is determined by disconnecting the chamber from the rest of the system so that the chamber is in a non-discharge state and then measuring impedance of the path from the feed plate to the ground via the shaft with an impedance meter while varying the oscillation frequency, the first series resonant frequency f_0 corresponding to a minimum impedance of the plasma processing chamber (1; Figure 1; column 5; line 44 - column 6; line 11), the minimum impedance evaluated with the plasma chamber (1; Figure 1;

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column 5; line 44 - column 6; line 11) disconnected from the plasma apparatus during a non-discharge period – claim 63

As stated above, Patrick (USPat. 5,474,648) teaches a plasma reactor (104, Figure 2a; column 6; line 54 – column 7; line 25) including a variable RF parameter sensor (202; Figure 2a) which measures power, voltage, current, phase angle, harmonic content (abstract), and impedance parameters at the plasma chamber electrode (112; Figure 2a, claim 5). That Patrick et al measures a frequency, resonant or otherwise, at the plasma chamber electrode is inherent because the applied frequency is that of the dynamic voltage and current that are measured and dynamically controlled (claim 6). The Examiner believes Patrick's apparatus is inherent in setting a frequency f_0 corresponding desired, or optimized values, including "corresponding" a minimum impedance (as measured by Patrick) of the plasma processing chamber. That Patrick can measure the minimum impedance with the plasma chamber disconnected from the plasma apparatus during a non-discharge period, is a claim requirement of intended use. See above.

Patrick further teaches that his plasma processing apparatus (Figure 2a; column 6; line 54 – column 7; line 25) produces frequencies which is defined by a capacitance between the plasma excitation electrode (112; Figure 2a) and a counter electrode (114; Figure 2a) for generating the plasma in cooperation with the plasma excitation electrode (112; Figure 2a). Further when the structure recited in the references is substantially identical to that of the claims, claimed properties or functions are presumed to be inherent. Where the claimed and prior art products are identical or substantially identical in structure or composition, or are produced by identical or substantially identical processes, a prima facie case of either anticipation or obviousness has

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been established. In re Best, 562 F.2d 1252, 1255, 195 USPQ 430, 433 (CCPA1977) – MPEP 2114.

Hoke teaches a cross flow deposition reactor (Figure 3) similar to Murata's cross flow deposition reactor (7; Figure 1). In particular, Hoke teaches a shower plate (12; Figure 3) at the gas introduction point (15; Figure 3) in the reactor (11; Figure 3).

It would have been obvious to one of ordinary skill in the art at the time the invention was made for Murata to use Patrick et al's system for plasma dynamic control including optimizing the relative frequencies between Murata's plasma excitation electrode and Murata's radio frequency generator depending on the geometry of the plasma chamber and dynamic processing conditions, further, for Murata and Patrick to add Hoke's shower plate (12; Figure 3).

Motivation for Murata to use Patrick et al's system for plasma dynamic control including optimizing the relative frequencies between Murata's plasma excitation electrode and Murata's radio frequency generator depending on the geometry of the plasma chamber and dynamic processing conditions is for enabling the repeatability and uniformity of plasma etching processes as taught by Patrick et al (column 3; lines 55-65), motivation Murata and Patrick to add Hoke's shower plate is for process gas diffusion under laminar flow as taught by Hoke (column 7; lines 54-65).

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It would be obvious to those of ordinary skill in the art to optimize the operation of the claimed invention (In re Boesch, 617 F.2d 272, 205 USPQ 215 (CCPA 1980); In re Hoeschele, 406 F.2d 1403, 160 USPQ 809 (CCPA 1969); Merck & Co. Inc. v. Biocraft Laboratories Inc., 874 F.2d 804, 10 USPQ2d 1843 (Fed. Cir.), cert. denied, 493 U.S. 975 (1989); In re Kulling, 897 F.2d 1147, 14 USPQ2d 1056 (Fed. Cir. 1990), MPEP 2144.05).

Response to Arguments

5. Applicant's arguments filed March 23, 2007 have been fully considered but they are not persuasive.

6. Applicant's arguments are based on Applicant's claim arguments which the Examiner, as stated above, believes are still intended use recitations of the claimed apparatus. To this topic, Applicant argues:

“

This is a structural limitation. The recited frequencies are not being "used" as suggested by the Examiner. The recited frequencies limit the structure of the plasma processing chamber to a specific configuration. Although broad in nature, the limitation defines the structural make-up of the plasma processing chamber. The plasma processing chamber being configured in a specific manner is not an intended use. Accordingly, claim 1 is allowable over the cited reference.

“

To this the Examiner disagrees. If the claimed frequencies are not being "used" as suggested by the Applicant, then what are the claimed frequencies with respect to the claimed apparatus? The Examiner believes these frequencies are result-effective process parameters as illustrated by the prior art: for example, Patrick (Column 2; lines 23-33) and Murata (Column 2; lines 33-50).

Conclusion

7. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

8. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Examiner Rudy Zervigon whose telephone number is (571) 272-1442. The examiner can normally be reached on a Monday through Thursday schedule from 8am through 7pm. The official fax phone number for the 1763 art unit is (571) 273-8300. Any Inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Chemical and Materials Engineering art unit receptionist at (571) 272-1700. If the examiner can not be reached please contact the examiner's supervisor, Parviz Hassanzadeh, at (571) 272-1435.


5/29/7